

EAST - [Untitled1:1]

File View Edit Tools Window Help

Drafts
Pending
Active
Failed
Saved
S1: (703) (710/316).CCLS.
S2: (597) SWITCH\$5 AND S1
Favorites
Tagged (10)
UDC
Queue
Trash

Search: List Browse Update Open

DBs: US-PGPUB;USPAT;USOCR;EPO;JPO;DERWENT;IBM_TDB

Default operator: OR

☒ Burels
☐ Highlight all hit terms initially

BRS form B&R form Image Text HTML

	U	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Cla	Inventor	S	C	P	2	3	Im
1	<input checked="" type="checkbox"/>	US 5485627 A	19960116	16	Partitionable massively parallel processing system	712/13	710/316		Hillis; W. Daniel	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
2	<input checked="" type="checkbox"/>	US 6092123 A	20000718	15	Method and apparatus for channel functions of a hardware packet switch	710/8	709/221; 709/238;		Steffan; Jeffrey Raymond et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
3	<input checked="" type="checkbox"/>	US 7058751 B2	20060606	41	Packet switch	710/317	710/28; 710/310;		Kawaral; Kenichi et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
4	<input checked="" type="checkbox"/>	US 6687781 B2	20040203	17	Fair weighted queuing bandwidth allocation system for slot control system with fixed sequence and dynamic slot eff	710/317	370/229; 370/232;		Wynne; John M. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
5	<input checked="" type="checkbox"/>	US 6115758 A	20000905	13	Microprocessor architecture with a switch network and an ar	710/36	709/213; 709/229;		Chen; Aphrodite	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
6	<input checked="" type="checkbox"/>	US 5941979 A	19990824	20	Dynamically determining a route through one or more switch	712/33	710/316		Lentz; Derek J. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
7	<input checked="" type="checkbox"/>	US 7017138 B2	20060321	73	Disk control apparatus	716/16	324/158.1; 324/763;		Zirojevic; Srdan et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
8	<input checked="" type="checkbox"/>	US 20030229757 A1	20031211	19	Storage subsystem with management site channel fun	711/114	710/316; 711/113;		Hosoya; Mutsumi et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
9	<input checked="" type="checkbox"/>	US 6535954 B2	20030318	20	Data transmitting/receiving method using distributed path c	711/114	709/239; 710/316;		Obara; Kiyohiro et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
10	<input checked="" type="checkbox"/>	US 5838937 A	19981117	17		710/316	370/351; 370/360;		Lee; Hyeun Tae et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US

Hits Details HTML